# Beyond Classification: Inferring Function Names in Stripped Binaries via Domain Adapted LLMs

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*Abstract*—Function name inference in stripped binaries is an important yet challenging task for many security applications, such as malware analysis and vulnerability discovery, due to the need to grasp binary code semantics amidst diverse instruction sets, architectures, compiler optimizations, and obfuscations. While machine learning has made significant progress in this field, existing methods often struggle with unseen data, constrained by their reliance on a limited vocabulary-based classification approach. In this paper, we present SYMGEN, a novel framework employing an autoregressive generation paradigm powered by domain-adapted generative large language models (LLMs) for enhanced binary code interpretation. We have evaluated SYMGEN on a dataset comprising 2,237,915 binary functions across four architectures (x86-64, x86-32, ARM, MIPS) with four levels of optimizations (O0-O3) where it surpasses the state-of-the-art with up to 409.3%, 553.5%, and 489.4% advancement in precision, recall, and F1 score, respectively, showing superior effectiveness and generalizability. Our ablation and case studies also demonstrate the significant performance boosts achieved by our design, *e.g.*, the domain adaptation approach, alongside showcasing SYMGEN's practicality in analyzing real-world binaries, *e.g.*, obfuscated binaries and malware executables.

# I. INTRODUCTION

Inferring function names for stripped binaries is crucial for many binary analysis and reverse engineering tasks, such as malware detection  $[45]$ , vulnerability discovery  $[39]$ ,  $[71]$ , decompilation [\[5\]](#page-13-0), and code similarity detection [\[54\]](#page-14-2). Binary function names act as a succinct abstraction of the semantics of functions embedded within the binary code. Through this abstraction, security professionals can efficiently comprehend the core semantics of functions, eliminating the need for a comprehensive and time-consuming reverse engineering process. For example, Mandiant, a recognized cybersecurity company, applies function name inference in binary malware analysis by automatically annotating function names in assembly code to better understand malicious behavior and develop effective countermeasures [\[67\]](#page-15-1).

Yet, function name inference for commercial-off-the-shelf (COTS) binaries presents substantial challenges. COTS binaries are compiled from varied compilation settings that encompass different computer architectures and optimization levels, thereby producing a variety of binary code. Complicating matters further, COTS binaries originate from source projects developed by individuals with diverse backgrounds and for a wide range of purposes, such as IoT firmware or malware [\[7\]](#page-13-1). This diversity in development origins introduces additional variability in the binary code and its contextual semantics [\[41\]](#page-14-3). Furthermore, a significant hurdle in function name recovery for COTS binaries is the binary stripping process, which results in the maximal semantic loss (*e.g.*, removing all the symbol names, except those used for dynamic linking, including function names, variable names, and debugging symbols if the program is compiled with these information). COTS binaries are also potentially fortified by protective measures, *e.g.*, obfuscation, impeding reverse engineering of binary code semantics.

Encouragingly, recent research [\[31\]](#page-14-4), [\[52\]](#page-14-5), [\[20\]](#page-13-2) has illuminated a promising direction in leveraging machine learning and language models to decode binary code semantics. However, these approaches often fall short of achieving a truly *generalizable* solution. Instead, they frequently yield models that tend to be overfitted on their training data but show poor performance on unknown data. For example, while SYMLM [\[31\]](#page-14-4) demonstrates promising performance, achieving an F1 score over 0.73 on its own test set, its effectiveness significantly diminishes when applied to previously unseen binaries, *e.g.*, test functions excluded from the training dataset, with performance decreasing to a mere 0.08 F1 score. This stark decline in performance underlines the models' poor generalizability, rendering them less effective for practical application on real-world COTS binaries. Moreover, a critical limitation of these approaches is their reliance on weak base models, such as LSTM [\[79\]](#page-15-2) for NFRE [\[20\]](#page-13-2), and BERT [\[17\]](#page-13-3) for SYMLM [\[31\]](#page-14-4) and XFL [\[52\]](#page-14-5). These models introduce high variance, particularly when they conceptualize the function name prediction task as a classification problem, inherently limiting their generalizability. The classification-based solutions confine the predictive model to selecting names and words from their training vocabularies, which represent only a small segment of the broader spectrum of natural language semantics.

Meanwhile, substantial advances are witnessed in the realm of machine learning for constructing generalizable models (*e.g.*, ChatGPT and Llama) that have achieved state-of-the-art performance in numerous tasks, such as question answering and natural language understanding [\[74\]](#page-15-3), [\[32\]](#page-14-6). A notable strategy underpinning the success of these models is the adoption of generative large language models (LLMs) that benefit from task-agnostic pre-training on extensive natural language corpora. Remarkably, these generative LLMs have shown promising results in zeroand one-shot learning scenarios, demonstrating significant performance on tasks previously unseen during training, with

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minimal or no task-specific data. Despite these advances, a critical limitation emerges when applying generative LLMs to the domain of binary code understanding: the inability to accurately grasp the "meaning" of binary code due to a lack of domain-specific knowledge [\[28\]](#page-14-7). The training datasets for these LLMs predominantly consist of natural language text with a small proportion of source code data [\[66\]](#page-15-4), [\[56\]](#page-14-8), devoid of the specialized content necessary to comprehend binary code semantics.

In this paper, we present SYMGEN, the first generative framework designed to infer function names for stripped binaries in a robust and generalizable manner. Unlike existing classification-based research, SYMGEN conceptualizes the function name prediction problem as an autoregressive generation task. This paradigm shift allows for a more subtle and flexible generation of function names, catering to the inherent complexities of binary code. To overcome the generalizability challenges, SYMGEN harnesses the capabilities of generative LLMs. Recognizing the conventional LLMs' shortfall in binary code comprehension, we employ a domain adaptation strategy specifically tailored to the binary code summarization task. This approach is crucial in bridging the knowledge gap, thereby equipping the LLMs with the required insight into binary code semantics. Furthermore, SYMGEN incorporates decompiler output, *i.e.*, decompiled code, for addressing the variability in binary code resulting from diverse compilation settings. Confronting the limitations posed by resource constraints—particularly the impracticality of full parameter training for LLMs—we adopt a parameter-efficient learning methodology within SYMGEN. This approach utilizes LoRAbased matrix decomposition  $[25]$  to optimize the training process, significantly reducing the computational resources required while maintaining the model's performance and scalability.

Our comprehensive evaluations reveal SYMGEN's exceptional generalizability and performance superiority over current state-of-the-art solutions across four architectures (x86-64, x86- 32, ARM, MIPS) and four optimization levels (O0-O3). Specifically, SYMGEN demonstrated remarkable improvements in precision, recall, and F1 score—outperforming the state-of-the-art by 409.3%, 553.5%, and 489.4%, respectively. Additionally, our tests on real-world binaries, including obfuscated binaries, IoT firmware images, and malware executables, have demonstrated SYMGEN's security practicality. For instance, when evaluated against obfuscated binaries, SYMGEN outperforms the state-ofthe-art by an astonishing 488% increase in F1 score. SYMGEN can also successfully predict malware's function names, e.g., get\_random\_ip, assisting malware analysis.

We have also evaluated the effectiveness of SYMGEN's components by ablation studies. First, when using decompiled code as input, SYMGEN achieves a 285.7% higher F1 score compared to using assembly code as input. Additionally, we find that domain adaptation with LLM-generated summaries improves the performance with increases of 6.36% in F1 score. With the parameter-efficient learning approach, the computational resource usage is significantly reduced, *e.g.*, the training duration is condensed to 37 hours on a single A100 GPU, which is considerably shorter than full-parameter training that takes 21 days on  $2,048$  A100 GPUs  $[66]$ . SYMGEN's advanced performance with our design and use of individual components further confirms its effectiveness and novelty.

Contributions. We make the following contributions:

- We propose a novel framework for binary function name inference, leveraging LLMs to achieve unprecedented levels of generalizability and accuracy.
- We present an advanced domain adaptation approach and a parameter-efficient learning strategy for tuning the pretrained generative LLMs to binary semantic modeling.
- Our work outperforms existing solutions in function name prediction, setting new generalizability benchmarks, with practical utility to obfuscated binary analysis. Our dataset and code are publicly available at [https://github.com/OSUSecLab/SymGen.](https://github.com/OSUSecLab/SymGen)

# II. BACKGROUND AND RELATED WORK

# *A. Preliminary*

Inferring function names from stripped binaries is a process of reverse engineering the semantics of binary functions and then generating accurate function names that reflect this function semantics. Unlike existing works  $[20]$ ,  $[31]$ ,  $[23]$  that define the problem as a classification problem, *i.e.*, selecting function name words from training vocabularies as the predicted function names, we define this problem as a *generation task*.

Formally, given the input binary function  $f$  ( $f \in F$ ) that can be tokenized into a sequence of binary code tokens, *i.e.*,  $f = \{t_1, t_2, ..., t_n \mid t_i \in V_f\}$ , the function name inference model M ( $M : F \rightarrow S$ ) generates an output sequence of natural language words  $s = \{w_1, w_2, ..., w_m \mid w_j \in V_s\}$  $(s \in S)$ . In this paper, the model M (*i.e.*, SYMGEN) follows an autoregressive generation paradigm, *i.e.*,  $p(s) = \prod_{j=1}^{m} p(w_j)$  $w_1, \ldots, w_{j-1}, f$ , where the generation of each word  $w_i$  depends on both the binary function semantics and the preceding words  $({w_1, ..., w_{i-1}})$ . This problem formulation is critical for boosting the *generalizability* of SYMGEN. Here, we define the model's generalizability as its performance on previously unseen functions that are not included in its training set.

# *B. Binary Reverse Engineering*

In software development, binary stripping is commonly used to remove meaningful debugging symbols from binaries, including function names and type information. This process, while beneficial for performance and security, leads to a remarkable reduction in semantic information within the binaries, posing significant challenges for reverse engineers in analyzing the underlying code semantics. To mitigate these challenges, many disassembly and decompilation techniques [\[4\]](#page-13-5), [\[68\]](#page-15-5) have been developed to bridge the gap created by the absence of these crucial symbols.

Specifically, disassembly translates binary code—consisting of machine-level code—back into assembly code, and the resulting assembly code is a format more intelligible to humans. Existing binary function name prediction works [\[31\]](#page-14-4), [\[52\]](#page-14-5), [\[20\]](#page-13-2), [\[14\]](#page-13-6), [\[33\]](#page-14-10) predominantly rely on the assembly code obtained by disassembling binaries. For example, SYMLM predicts function names from assembly code along with a tracebased model [\[31\]](#page-14-4). ASMDEPICTOR encodes binary function semantics from assembly code using a Transformer model [\[52\]](#page-14-5). Decompilation goes a step further. It recovers the missing semantic information and generates a roughly equivalent source code or pseudo-code, which is typically in a form resembling C, albeit approximated.

Recently, we have also observed an increasing trend of using machine learning for symbol recovery tasks, *e.g.*, function

name prediction [\[14\]](#page-13-6), [\[20\]](#page-13-2), [\[31\]](#page-14-4), [\[33\]](#page-14-10), [\[52\]](#page-14-5), [\[51\]](#page-14-11) and variable name/type inference  $[23]$ ,  $[10]$ ,  $[48]$ ,  $[53]$ ,  $[78]$ , which could enhance decompiler output. For example, DIRTY [\[10\]](#page-13-7) models the local decompiled code semantics for predicting variable names and types. NERO [\[14\]](#page-13-6) grasps the function semantics using convolutional neural networks. Note that, *function name prediction is different from the other binary reverse engineering tasks, e.g., variable and type inference, as it requires modeling the global semantics of functions*.

#### *C. Generative LLMs*

Generative LLMs are models that produce output based on input sequences, following the autoregressive generation paradigm, *i.e.*, the generation of tokens depends on the previously generated tokens [\[66\]](#page-15-4). These models can be categorized into encoder-decoder models and decoder-only models based on model architectures [\[74\]](#page-15-3), [\[29\]](#page-14-14). The encoder-decoder model, adopting the Transformer architecture, involves two separate components for processing input and output [\[57\]](#page-14-15). The encoder processes the input data and encodes it into a context-rich representation, while the decoder generates output based on this representation. The decoder-only models, on the other hand, are based solely on the Transformer decoder module [\[56\]](#page-14-8), [\[66\]](#page-15-4). They are particularly known for their effectiveness in understanding and generating texts. The GPT model family (including the well-known ChatGPT and GPT-4) by OpenAI is a prime example of a decoder-only model [\[56\]](#page-14-8), [\[47\]](#page-14-16), [\[1\]](#page-13-8). The decoder-only model architecture, while simpler compared to encoder-decoder models, proves effectiveness in a wide range of tasks [\[62\]](#page-14-17), [\[74\]](#page-15-3). More importantly, decoder-only models have exhibited substantial capabilities in terms of generalizability under the zero- or few-shot setting  $[6]$ , distinguishing them from other generative language models. In addition to the generative LLMs, there are also language models that leverage the encoder-only model architecture, *e.g.*, BERT [\[17\]](#page-13-3). These models are specifically designed for encoding input, but they are not primarily geared towards generating text. In function name prediction, existing LLM-based solutions mainly adopt the encoder-only model  $[31]$ ,  $[52]$ .

# III. OVERVIEW

## *A. Challenges*

C1: Generalizable Function Name Inference. The generalizability of function name inference models is very important in dealing with COTS binaries in real-world reverse engineering tasks. Yet, these binaries, compiled from a variety of source projects infused with domain-specific knowledge from multiple fields, present a significant challenge in learning their semantics. This complexity is compounded by the fact that COTS binaries are compiled from source projects crafted by developers with distinct backgrounds and intended for various production purposes. Additionally, COTS binaries, commonly stripped, carry very little semantic information which makes learning and reverse engineering their semantic representations especially hard. Furthermore, the diversity of binary code is also mirrored in the output of the models; that is, the range of function names that need to be predicted is also varied [\[31\]](#page-14-4).

C2: Knowledge Gaps between Natural Language Tasks and Function Name Inference. While generative LLMs, such as ChatGPT and Llama, exhibit promising potential for our task, their efficacy in comprehending binary code is

prohibitively poor [\[28\]](#page-14-7). This limitation significantly impedes their direct application in inferring function names [\[28\]](#page-14-7). Unlike the objectives of many generative tasks, such as question answering where the models excel, function names are highly abstract and encapsulate the core semantics of functions in a very succinct manner. Consequently, the substantial knowledge gaps between the natural language processing capabilities of generative LLMs and the specific requirements for interpreting binary code in our task present a formidable challenge.

C3: Resource-expensive Training of LLMs. The advanced performance of LLMs presents a promising direction for function name inference. However, this potential is tempered by the extraordinarily high costs associated with hardware resources, time, and data availability required for model training. The prevalent practice of full-parameter training demands substantial resources; for instance, training the Llama model necessitates the use of 2048 NVIDIA A100 GPUs and 21 days [\[66\]](#page-15-4). Additionally, there is an absence of sufficiently large binary datasets that can satisfy the requirements for training these LLMs. The creation of a high-quality binary dataset with accurate ground truth annotations entails significant human efforts. Consequently, employing LLMs for our task poses notable challenges in model training.

C4: Data Leakage and Duplication. Data leakage—test binary functions also appear in the training set—significantly affects the accuracy of evaluating generalizability. Additionally, the training set may contain popular but duplicated binary functions, leading to model overfitting on these functions and reducing performance on new, unseen samples [\[9\]](#page-13-10), [\[37\]](#page-14-18). Moreover, using well-trained models as a base for training can also introduce leakage through their pretraining process. Detecting this leakage and identifying duplicates is extremely challenging for several reasons. First, stripped binary functions compiled from the same source code can appear different due to factors like varying binary symbol addresses (introducing unique address-specific callee function names). Second, the pretraining datasets for these well-trained models are typically closed-source, making it difficult to track potential overlaps.

#### <span id="page-2-0"></span>*B. Key Insights*

S1: Autoregressive Function Name Inference with Generative LLMs. Existing generative LLMs demonstrate exceptional generalizability, attributable to their autoregressive training paradigm. In contrast, the base models for prior function name prediction efforts are predominantly trained using the masked language modeling objective, employing encoder-only models pre-trained in this manner. This approach allows the model to consider both preceding and succeeding contexts during prediction, but such a setting does not hold for realworld applications because the succeeding context is missing. In contrast, autoregressive training and inference accurately mirror the nature of real-world generation processes, thereby offering enhanced generalizability [\[6\]](#page-13-9). Consequently, we apply autoregressive training to SYMGEN.

S2: Domain-adaptive Learning with Function Summaries. To address C2, a straightforward method involves directly finetuning the model on a binary function name inference dataset. This method, however, faces limitations due to the inherent abstractness of function names, which encapsulate only a fraction of the function's semantics. And different function

```
1 /* Return the address of the last file name component of
  → NAME. If NAME has no relative file name components
  → because it is a file system root, return the empty
  \leftrightarrow string. */2
3 char * last_component (char const *name)
 \{5 char const *base = name + FILE_SYSTEM_PREFIX_LEN
       \rightarrow (name);
6 char const *p;
      bool last_was_slash = false;
8
9 while (ISSLASH (*base))
10 base++;
11
12 for (p = base; *p; p++)
13 \qquad14 if (ISSLASH (*p))<br>15 last was slas
             last_was_slash = true;
16 else if (last_was_slash)
17 {
18 base = p;
19 last_was_slash = false;
20 }
21 }
2223 return (char *) base;
24 }
```
Figure 1: Function Summary of **last\_component** from GNU Sed Project

names may emphasize various aspects of a function's semantics, leading to inconsistencies in semantic representation. For example, [Figure 1](#page-3-0) presents the last\_component function, in which the function summary (at line 1) describes retrieving the last component of a *file path*—a detail not explicitly captured by its name. This discrepancy underscores that function summaries, typically presented as comments, are rich in semantic information, detailing function behavior in natural language, thereby playing to the strengths of LLMs. Such summaries can serve as a crucial bridge to narrow the knowledge between binary function semantics and the descriptive richness required for accurate interpretation.

S3: Parameter-efficient Learning. In addressing C3, we leverage a parameter-efficient training methodology customized to our target generative LLMs. Using the fine-tuning optimization  $[16]$ ,  $[25]$ , we freeze the weights of the pre-trained model and employ a gradient decomposition technique to produce low-rank representations of these weights. These lowrank weights significantly reduce the parameter count compared to the original model weights, thus enhancing computational efficiency. Notably, the attention and feed-forward layers account for a substantial portion of the model's size and demand considerable resources for updates. Consequently, we specifically apply parameter reduction and integrate the resultant low-rank weights into the model via residual connections, optimizing performance without losing the model's original capacities (*e.g.*, the instruction following ability).

S4: Data Deduplication and Leakage Mitigation. For C4, we have proposed systematic methods to reduce data duplication and leakage. We start by defining the leakage and duplication problems from two angles: in-dataset and cross-dataset duplication. With this framework, we identify duplicates by (*i*) matching the same source function names or function bodies during compilation, and (*ii*) detecting duplicated but syntax-different binary functions compiled from the same source functions using callee normalization. To address leakage in the base model, we apply the prevalent membership inference technique to identify potential leakage in its pretraining.

# *C. System Overview*

The workflow of SYMGEN consists of three major steps:

(I) Domain adaptation by function summarization involves the strategic training of SYMGEN's base model to enhance its knowledge of binary code using function summaries.

(II) Parameter-efficient learning minimizes the number of training parameters for both domain adaptation and function name inference tasks by layer weight selection, weight freezing, matrix decomposition, and residual connections.

(III) Finetuning for function name generation transfers the knowledge from the domain-adapted model, as developed in Step (I), to the task of inferring function names.

#### IV. DETAILED DESIGN

# <span id="page-3-1"></span>*A. Domain Adaptation*

Our objective is to develop a *generalizable* model capable of learning binary code semantics and generating accurate names for binary functions. To this end, we exploit the capabilities of pre-trained generative LLMs to grasp the complex and dynamic semantics of binary code as described in [§III-B.](#page-2-0) To leverage these models for binary function name inference, the intuitive methods include zero-shot (or few-shot) prompting and direct finetuning for our task, where finetuning usually can boost the models' performance more. However, as revealed through our evaluations in [§VI-D,](#page-9-0) the straightforward fine-tuning achieves the desired efficacy, with the latter resulting in a performance that is 8.21% inferior to our proposed approach. Our analysis indicates that this is due to the models' limited exposure to binary code during training, as they are predominantly developed on natural language corpora [\[6\]](#page-13-9), [\[66\]](#page-15-4). Although these corpora may include a minor set of source code (*e.g.*, 5% in Llama [\[66\]](#page-15-4)), source code is fundamentally different from binary code, where source code is with rich semantics, *e.g.*, function and variable names, data types, and structural details. This difference leads to a significant knowledge gap between the models' pre-training on natural language (and to some extent, source code) and the specific requirements of our binary code comprehension task, thereby resulting in the observed performance inefficiencies. To mitigate this knowledge gap of LLMs, we propose to perform domain adaptation by training the models on summarizing decompiled code.

[Figure 2](#page-4-0) presents the workflow of SYMGEN's domain adaptation approach. Our initial step involves curating opensource C projects, which have been extensively utilized in prior learning-based binary analysis research [\[48\]](#page-14-12), [\[31\]](#page-14-4), [\[52\]](#page-14-5). These projects are then compiled into binaries, with debugging symbols to facilitate analysis across diverse computer architectures and optimization levels. Subsequent to binary stripping, the generated stripped binaries are decompiled into decompiled code. To adapt the LLMs on binary code comprehension, we need to obtain the dataset with ground truth, *i.e.* the description of binary code semantics. For this, we initially sought to align with methodologies from source code summarization research [\[2\]](#page-13-12), which typically uses function comments as ground truth. However, we observe that the noise within these function comments poses a significant challenge,

<span id="page-4-0"></span>

Figure 2: Domain Adaptation by Function Summarization

potentially compromising SYMGEN. In this paper, we propose two approaches to address this challenge, including (1) function comment preprocessing and noise removal, and (2) summary generation using code LLMs.

Function Comment Preprocessing and Noise Removal. To understand the noises in function comments, we first perform a manual study on the function comments of 900 randomly sampled source functions from 45 open-source projects. Overall, we have identified five categories of noises, stemming from varying coding conventions and assumptions, as well as the often ad-hoc nature of development processes and practices. For example, one category of errors is the comment used for communication, instead of summarizing the function semantics, where developers use keywords like "TODO" and "FIXME" to communicate with others. More details of these noises are provided in Appendix [§A](#page-15-7) for interested readers.

In order to remove the noises from the function comments and form a dataset with high-quality ground truth, we have followed the noise-removal solutions from existing code comment cleaning research [\[61\]](#page-14-19). Specifically, we run their open-sourced artifact<sup>[1](#page-4-1)</sup> on our source code dataset. This process involves two steps: (1) identifying the source functions and their associated function comments using abstract syntax tree (AST) parsing, and (2) cleaning these comments using the rule-based artifact  $[61]$ . For step (1), we have provided more details of our engineering efforts in Appendix [§B.](#page-15-8) Note that, for function comments, we consider the comment, right above the function signature, as our target and directly extract it from source files for each function.

Summary Generation via Code LLMs. In addition to the approach of function comment preprocessing, we have also explored the use of LLMs for generating high-quality function summaries for domain adaptation. Our design choice is motivated by recent advancements in code LLMs, such as Code Llama [\[60\]](#page-14-20) and StarCoder [\[38\]](#page-14-21), which demonstrate impressive capabilities in generating succinct, high-quality source code summaries. Moreover, it has been found that the output generated by these powerful LLMs can serve as highquality references for training other LLMs [\[72\]](#page-15-9). For example, ShareGPT is a set of dialogue corpus, generated by ChatGPT, which has been widely used for training other LLMs [\[13\]](#page-13-13), [\[18\]](#page-13-14). Therefore, we opt to use Code LLMs to generate summaries for binary functions via understanding their source code.

While code LLMs can produce seemingly accurate comments, a major concern is their potential for hallucination, *i.e.*, generating summaries that express incorrect or nonexistent semantics. To address this issue, we observe that LLMs can self-improve, *i.e.*, they can correct their own errors via reasoning  $[27]$ . Therefore, we have adopted techniques from prior studies of self-consistency checking [\[72\]](#page-15-9), [\[12\]](#page-13-15), [\[42\]](#page-14-23). Specifically, we employ the universal self-consistency method [\[12\]](#page-13-15), a straightforward yet effective approach to eliminate hallucinations. We query the LLM multiple times to produce several summary candidates. We then use the LLMs again to evaluate these candidates and select the most consistent summary among them as the final output. Through a systematic series of experiments involving various prompts, we identify the most effective prompt for our task, as shown in [Figure 9a](#page-16-0) (in Appendix  $\S$ C). In this process, we select and use the Code Llama model, which is fine-tuned from the Llama 2 model, for its state-of-the-art performance on code understanding [\[60\]](#page-14-20).

To this end, we have proposed two approaches to address the long-standing issue of noise in function comments [\[77\]](#page-15-11), [\[11\]](#page-13-16), to the best of our efforts. Our subsequent evaluations indicate that (1) both approaches can boost SYMGEN's effectiveness, and (2) the summary generation-based approach is more effective for SYMGEN, *e.g.*, SYMGEN with the summary generationbased approach has 5.81% better precision than SYMGEN with preprocessed comments (see  $\frac{\text{SVI-D}}{\text{O}}$ ). Consequently, we have selected summary generation as our default solution for domain adaptation. With the decompiled code and generated ground-truth summaries of binary functions, we proceed to train LLMs for domain adaptation using a parameter-efficient learning approach.

# *B. Parameter-efficient Learning*

Existing binary function name prediction models, *e.g.*, SYMLM [\[31\]](#page-14-4) and XFL [\[52\]](#page-14-5), commonly involve full-parameter supervised finetuning. This method essentially fits models to suit a specific binary code dataset. However, this fine-tuning process updates all parameters of the model, demanding significant computational resources, *e.g.*, it takes 8 days for SYMLM's finetuning as reported by the paper [\[31\]](#page-14-4). In SYMGEN, we leverage more advanced generative LLMs, which have significantly more parameters than the base models of SYMLM and XFL, *i.e.*, BERT [\[17\]](#page-13-3). Therefore, this inefficiency leads to substantial challenges in training resources and time. To address this issue, numerous efficient finetuning efforts have been proposed. For example, parameter-frozen tuning integrates tunable low-rank matrices into model layers and has shown superior performance compared to full-parameter finetuning [\[69\]](#page-15-12), [\[25\]](#page-14-9). Observing these advances, we leverage a parameter-efficient learning framework based on LoRA [\[25\]](#page-14-9), as shown in [Figure 3.](#page-5-0) In particular, we focus on the attention and feed-forward layers of SYMGEN and perform low-rank matrix decomposition and residual connection on the frozen layer weights.

Layer Weight Selection. SYMGEN is built upon the generative LLM using the transformer decoder model architecture. This model architecture consists of four major layers, including (i) masked multi-head attention layers,  $(ii)$  multi-head attention layers,  $(iii)$  add & norm layers, and  $(iv)$  feed-forward layers. Different from BitFit [\[76\]](#page-15-13) and DiffPruning [\[21\]](#page-13-17), which selectively adjust a subset of the model's layers, SYMGEN's training focuses on layers with trainable weights. That is, we

<span id="page-4-1"></span><sup>&</sup>lt;sup>1</sup>[https://github.com/BuiltOntheRock/FSE22](https://github.com/BuiltOntheRock/FSE22_BuiltOntheRock)\_BuiltOntheRock

<span id="page-5-0"></span>

Figure 3: Parameter-Efficient Learning. In the Transformer decoder model, we train the low-rank weights for the multihead attention layers and feed-forward layers.

exclude the add & norm layers, which inherently have no trainable parameters. This design choice of tuning all learnable weights is to optimize SYMGEN to better understand binary code semantics. For this, we focus on SYMGEN's attention layers (*i.e.*, masked multi-head attention layers and multihead attention layers) and feed-forward layers. One of the shared features of these layers is that they have the same mechanism of performing dot products of layer weights and the input. Formally, given a sequence of decompiled code tokens  $X = \{x_1, x_2, ..., x_n\}$ , the attention layer produces the key, value, and query matrices, *i.e.*  $K$ ,  $V$ , and  $Q$  by:

$$
k_i = W_k \cdot x_i, \quad q_i = W_q \cdot x_i, \quad v_i = W_v \cdot x_i \tag{1}
$$

where  $k_i \in K$ ,  $q_i \in Q$  and  $v_i \in V$  for  $1 \leq i \leq n$ .  $W_k \in \mathbb{R}^{d \times d}$ ,  $W_q \in \mathbb{R}^{d \times d}$ , and  $W_v \in \mathbb{R}^{d \times d}$  are learnable weight matrices. The feed-forward layer essentially encapsulates dot product calculation, *i.e.*,  $O = W_o \cdot X'$ . In this paper, we focus on tuning the weights of  $W_k$ ,  $W_q$ ,  $W_v$ , and  $W_o$  as illustrated in [Figure 3.](#page-5-0)

Low-rank Weight Decomposition. In deep neural networks, the weight updates are performed by the backward propagation process, in which the gradients of layer weights are calculated to update themselves  $[35]$ . For parameter-efficient training, we use the low-rank matrix decomposition approach proposed by existing parameter-frozen training research [\[25\]](#page-14-9), [\[16\]](#page-13-11). As shown in [Figure 3,](#page-5-0) we first freeze the original model weights  $W$  $(W \in \mathbb{R}^{d \times d}$  can be any of  $W_k$ ,  $W_q$ ,  $W_v$ , and  $W_o$ ). Freezing the model weights can not only reduce the computational resources required for training but also preserve the instruction-following capacities of our target pretrained models. Next, SYMGEN creates the low-dimension trainable weight by decomposing each frozen weight into two low-rank matrices ( $W^{\nabla} \in \mathbb{R}^{d \times r}$  and  $W^{\Delta} \in \mathbb{R}^{r \times d}$  which have far less parameters compared with the original weights. Specifically, given the frozen weight  $W$ , the backward propagation generates the gradient  $\delta W \in \mathbb{R}^{d \times d}$ . SYMGEN decomposes this gradient into  $W^{\nabla}$  and  $W^{\Delta}$ , so that  $\delta W = W^{\nabla} \cdot W^{\Delta}$ . In these low-rank weights, r is far less than d, *i.e.*,  $r \ll d$ , which decreases the number of parameters that need to be trained from  $d \times d$  to  $2 \times r \times d$ . This decomposition can dramatically reduce the trainable weights (*e.g.*, our training on the 13.5 GB Llama model only requires 20.3 MB weight updates). Eventually, the low-rank matrices are integrated into attention and feed-forward layers by residual connections [\[24\]](#page-14-25).

#### <span id="page-5-2"></span>*C. Finetuning for Function Name Inference*

SYMGEN aims to accurately infer function names for stripped binaries. To achieve this, we finetune the LLM that has been adapted to the binary code understanding domain ([§IV-A\)](#page-3-1) on binary function name inference dataset.

Input Masking. As discussed in [§III-B,](#page-2-0) SYMGEN takes as input the decompiled code from stripped binaries and generates function names. This decompiled code carries the artificial function names generated by decompilers, such as FUN\_080021e where 080021e is the function address. We observe that simply feeding the model with the raw decompiled code can hurt its performance since the model to be finetuned can be misled by the artificial function names. One simple solution is to add instructions to let the model avoid focusing on these artificial names but this method cannot effectively address the problem due to two reasons. First, artificial function names vary as the function addresses change. Second, the function body can also include artificial function names for callees. Considering these noises, we formalize the fine-tuning task as a cloze-solving problem [\[63\]](#page-14-26), *i.e.*, we mimic the process of how human beings learn the semantics of words by the context. Similarly, in SYMGEN, binary function names are inferred and generated by understanding the context information of the function semantics. To achieve this, the artificial function name in decompiled code is replaced by the [MASK] token without altering any other portion of the decompiled code (*e.g.*, function body and function parameters). It is worth noting that the [MASK] token is widely used in pretraining LLMs. The resultant decompiled code is used as the input to SYMGEN. For readers' interest, [Figure 9c](#page-16-0) (in Appendix [§C\)](#page-15-10) presents SYMGEN's prompt for completing the cloze-solving task.

Finetuning Ground Truth and Objective. For finetuning, we have used the developer-created function names as ground truth, following the common practice as existing research [\[31\]](#page-14-4), [\[52\]](#page-14-5), [\[20\]](#page-13-2), [\[14\]](#page-13-6). Specifically, we obtain it from the DWARF entries of the binaries with debugging symbols since we have full control of the dataset generation process. To process the ground truth names into tokens of the SYMGEN's base model vocabulary, we use the BPE tokenizer<sup>[2](#page-5-1)</sup> downloaded along with our base model, *i.e.* Code Llama. To train the model on our binary function name generation dataset, we formalize the finetuning process as the autoregressive generation task. Formally, for a tokenized input sequence  $\mathbf{x} = \{x_1, x_2, x_3, ..., x_n\}$  and output sequence  $\mathbf{y} =$  $\{y_1, y_2, y_3, ..., y_m\}$ , SYMGEN aims to maximize the likelihood following the forward autoregressive factorization model:

$$
\max_{\theta} \log P_{\theta}(\mathbf{y}) = \sum_{i=1}^{m} \log P_{\theta}(y_i \mid \mathbf{x}; y_{1:i-1}) \tag{2}
$$

<span id="page-5-1"></span><sup>2</sup>We use Huggingface tokenizers that comes with SYMGEN's base model.

$$
\log P_{\theta}(y_i \mid \mathbf{x}; y_{1:i-1}) = \frac{\exp(h_{\theta}(\mathbf{x}; y_{1:i-1})^\top e(y_i))}{\sum_{y'} \exp(h_{\theta}(\mathbf{x}; y_{1:i-1})^\top e(y'))} \quad (3)
$$

where  $y_{1:i-1}$  denotes the preceding tokens of  $y_i$ .  $h_\theta(\cdot)$  is the hidden states produced by SYMGEN's intermediate layers for context representation.  $e(\cdot)$  is the input embedding.

# V. SYMGEN DATASET AND LEAKAGE MITIGATION

We collect 33 open-sourced C projects from GNU Software and widely used libraries, including those that have been exten-sively used by existing binary reverse engineering research [\[31\]](#page-14-4), [\[20\]](#page-13-2), [\[52\]](#page-14-5), *e.g.*, OpenSSL, binutils and bash (the detailed list of projects is presented in [Table VI](#page-16-1) of Appendix [§D\)](#page-16-2).

#### *A. Dataset Construction*

Function Name Prediction Dataset. We compile above source projects into four computer architectures (x86-32, x86-64, ARM, and MIPS) and four optimization levels (O0, O1, O2, and O3) using GCC-9.4.0. Our dataset contains 9,842 unique binaries and 2,237,915 binary functions in total. We randomly split our dataset into training, validation, and test sets with the ratio of 8:1:1 at the binary level, same as prior works [\[14\]](#page-13-6), [\[31\]](#page-14-4). Note that, the binary compilation renders both binaries with and without debugging information. To construct datasets for our task, we obtain the input decompiled code from stripped binaries and the ground truth function names by analyzing the DWARF entries from binaries with debugging information.

Domain Adaptation Dataset. Using compiled binaries, we also construct our domain adaptation datasets by the two approaches proposed in [§IV-A,](#page-3-1) *i.e.*, (1) function comment preprocessing and noise removal, and (2) summary generation via code LLMs. After applying our proposed steps to obtain the function comments and LLM-generated summaries, we observe a common problem of matching source function comments/summaries and binary functions, because the comments and summaries are obtained at the source level, not from binaries. To address this issue, we connect source function comments/summaries and stripped binary functions by establishing connections using binary function addresses. Specifically, after obtaining the function comments or summaries  $(\S$ IV-A), we first connect the source and binary functions by matching source function names and the binary function names from the DWARF entries of binary with debugging information. Next, we further parse the DWARF entries to connect the source function names and the binary function start and end addresses. As the start and end addresses of a function remain unchanged regardless of whether it is stripped, we link stripped and unstripped binary functions by matched function addresses. To this end, we successfully match the function comments/summaries with its corresponding stripped binary functions.

#### <span id="page-6-1"></span>*B. Data Leakage Mitigation and Deduplication*

In real-world binaries, there are functions that inevatably used across projects for reasons such as code reuse and the use of libraries, *e.g.*, binutils binaries share a large amount of code that is copied. We observe that the datasets of existing works, *e.g.*, SYMLM [\[31\]](#page-14-4), commonly contain many duplicated functions in the test set which also occurs in the training set. In machine learning evaluations, these duplicated samples are defined as data leakage [\[22\]](#page-13-18), *i.e.*, the duplicated functions have been leaked to the model during training. While facing this issue, we observe that existing function name inference

<span id="page-6-0"></span>

(a) ASMDEPICTOR's Training Sample (Line 37,492 in its Training File)



(b) ASMDEPICTOR's Test Sample (Line 27 in its Test File)

Figure 4: Data Leakage in ASMDEPICTOR. The two samples, compiled from the same source function but in different function addresses, are treated as different samples.

works do not fully resolve it. In particular, SYMLM [\[31\]](#page-14-4) and XFL [\[52\]](#page-14-5) do not consider data leakage in their design. ASMDEPICTOR [\[33\]](#page-14-10) removes duplicates by matching the input code exactly, which unfortunately fails to identify duplicate samples that differ slightly. For example, [Figure 4](#page-6-0) presents code snippets of two functions from ASMDEPICTOR's training and test sets, respectively. These two functions originate from the same source function but from different binaries, resulting in distinct symbol addresses. Consequently, the highlighted addresses in lines 1 and 3 differ between these two functions. Unfortunately, ASMDEPICTOR ignores their equivalence due to these superficial differences.

Dataset Duplicates and Leakage. In this paper, we have observed two types of dataset duplicates: (1) cross-dataset duplicates and (2) in-dataset duplicates. Cross-dataset duplicates are defined as binary functions that appear in both the training and test sets, which is commonly defined as data leakage. In-dataset duplicates refer to functions that appear more than once within the training set or the test set, such as syncok and copy\_termtype showing several times in our training set. For the purpose of generalizability evaluation, it necessitates removing the leaked samples, *i.e.*, cross-dataset duplicates, from our test sets. However, the question of whether deduplicating in-dataset duplicates within the training set (or test set) is necessary remains unanswered. Such a question was unfortunately ignored by prior research and they keep in-dataset duplicates in both training sets and test sets. In this paper, we have conducted ablation studies to validate the performance of both SYMGEN and our baselines with and without indataset duplicates in [§VI-D.](#page-9-0) Our rigorous evaluations show that removing in-dataset duplicates from the training sets can effectively address overfitting and improve model performance by 2.94×. Therefore, unless otherwise specified, we remove in-dataset duplicates in our subsequent evaluations by default.

Data Deduplication and Leakage Mitigation. Based on these observations, we employ a more rigorous deduplication

Algorithm 1: Callee Normalization in Stripped Decompiled Code

<span id="page-7-0"></span> Function visit(*node*): switch *node.type* do **case** *expression*: visitExpression(*node*) 5 ... **initialize**  $\text{funcMap} := \{\}$  Function visitPostfixExpression(*node*): if *node.primaryExpression* exists and *node.LeftParen* exists then *callFunName* := *node.primaryExpression.Identifier* if *callFunName* in *funcMap* then *callFunName* := *funcMap*[*callFunName*]  $12$  else *newFunName* := "FUN\_" + *funcMap.count funcMap*[*callFunName*] := *newFunName callFunName* := *newFunName* return visit(*node.children*);

approach to mitigate in-dataset and cross-dataset duplicates. To be more thorough, we perform deduplication on two levels: (*i*) duplicates with the same function names or binary code, and (*ii*) duplicates with the same source function but different binary code due to different addresses, as shown in [Figure 4.](#page-6-0) For level (*i*), we remove binary functions that either share the same source function names or have different names but identical function bodies. For level (*ii*), our approach is motivated by the observation of examples in [Figure 4.](#page-6-0) Specifically, the decompiled code of stripped binaries often contains address-related callee function names in its call statement. These addresses in callee names can appear different due to variations in the memory layout of different binaries, resulting in duplicated decompiled functions with distinct function bodies. To avoid duplicates that ASMDE-PICTOR failed to identify, we propose [algorithm 1](#page-7-0) to normalize callee function names for deduplication. Specifically, we first parse the decompiled binary functions from stripped binaries to generate their abstract syntax trees (ASTs). Next, as shown in lines 1 to 5, we perform a depth-first search on the generated AST to traverse all nodes. Finally, we normalize the callee function names by substituting them with placeholders using the visitPostfixExpression function (lines 6 to 16). An example of decompiled code before and after applying [algorithm 1](#page-7-0) is provided in [Figure 10a](#page-17-0) and [Figure 10b](#page-17-0) (in Appendix  $\S$ E). It is worth noting that our approach might aggressively filter out false positive cases, *e.g.*, functions having the same names but different semantics. However, we argue that such a rigorous approach will result in a reliable dataset to accurately reflect SYMGEN's generalizability. Eventually, our approach identifies 77.9% duplicated functions (*i.e.*, 1,742,421 binary functions). The resultant dataset contains 495,494 unique binary functions, which is comparable in size to the dataset of existing research [\[52\]](#page-14-5).

Leakage Detection in SYMGEN's Base Model. SYMGEN is trained based on the Code Llama model [\[60\]](#page-14-20), and the samples of our test set can be potentially leaked in Code Llama's training process. However, its pertaining dataset is unfortunately closed-source, posing significant challenges in detecting this leakage. Although Code Llama is mostly trained on source code, which is significantly different from the stripped binary code in our test set, we argue it is crucial to detect such leakage to ensure the quality of our dataset. For this, we have applied a membership inference approach, prevalent in previous dataset inference work  $[8]$ ,  $[40]$ . Specifically, we prompt our base model to complete the incomplete stripped binary code of our test sets and measure its completion effectiveness. Following the common setting [\[8\]](#page-13-19), [\[40\]](#page-14-27), we prepare the incomplete stripped binary code by removing the last 5 lines of each decompiled function while the average number of lines is 32. We use the popular metrics, *i.e.*, exact matching and CodeBLEU [\[58\]](#page-14-28), as the completion metric. The evaluations of our test datasets on Code Llama present scores of 0 in exact matching, indicating that none of the test samples are completed. Additionally, the average CodeBLEU similarity score is only 0.098, where CodeBLEU is computed by n-gram matching that matches the variable names and symbols, and we find that generated lines only match some symbols without producing complete lines. To this end, we argue that our test sets are not leaked to SYMGEN's base model in its training process.

# VI. EVALUATION

<span id="page-7-3"></span>We have implemented SYMGEN with 3,068 lines of own code using the pretrained Code Llama with its own BPE tokenizer $3^{\circ}$  $3^{\circ}$  as the base model for SYMGEN, which is codespecific version of Llama 2 and finetuned on six programming languages, *e.g.*,  $C++$  [\[60\]](#page-14-20). We employ Ghidra [\[44\]](#page-14-29) to parse binaries and generate decompiler output. It is important to underscore that SYMGEN's design is agnostic to decompilers. We opt to use Ghidra due to its open-source nature and its widespread recognition and adoption. We develop the source and decompiled code parsers based on ANTLR 4 [\[49\]](#page-14-30). For parameter-efficient learning and finetuning, we have developed the training pipeline on top of Pytorch [\[50\]](#page-14-31), transformers [\[73\]](#page-15-14), alpaca-lora  $[65]$ , and loralib  $[26]$ .

Our evaluations aim to answer the following research questions:

- RQ1: What is SYMGEN's overall performance, *i.e.*, across different optimization levels and architectures?
- RQ2: Compared with the state-of-the-art, how effective and generalizable is SYMGEN?
- RQ3: How can SYMGEN's components and strategic design, *e.g.*, domain adaptation, improve its performance?
- RQ4: Can SYMGEN predict function names for real-world binaries, *e.g.*, obfuscated binaries and malware executables?

#### <span id="page-7-2"></span>*A. Experimental Setup*

Evaluation Environment. Our evaluations are performed on a server equipped with a 48-core AMD EPYC 7643 processor at 2.3 GHz, 921 GB memory, and four NVIDIA A100 GPUs with 80 GB VRAM each, running RHEL 8.6 OS.

Evaluation Metrics. We adopt the evaluation metrics, *i.e.*, precision, recall, and F1 score, that are commonly used by prior works [\[31\]](#page-14-4), [\[20\]](#page-13-2), [\[14\]](#page-13-6). Given the ground truth function name S and inferred function name  $\hat{S}$ , we split them into individual

<span id="page-7-1"></span><sup>&</sup>lt;sup>3</sup>The model is downloaded from Huggingface: [https://huggingface.co/](https://huggingface.co/codellama) [codellama](https://huggingface.co/codellama)

words, *i.e.*,  $S = \{w_1, w_2, ..., w_n\}$  and  $\hat{S} = \{\hat{w_1}, \hat{w_2}, ..., \hat{w_n}\}$ and then calculate precision, recall and F1 score by:

$$
TP = \sum_{\hat{w_i} \in \hat{S}} 1_S(\hat{w_i}), FP = \sum_{\hat{w_i} \in \hat{S}} 1 - 1_S(\hat{w_i}), FN = ||S|| - TP
$$
  

$$
1_S(\hat{w_i}) = \begin{cases} 1 & \text{if } \hat{w_i} \in S \\ 0 & \text{if } \hat{w_i} \notin S \end{cases}
$$
  
Precision = 
$$
\frac{TP}{TP + FP}, \quad \text{Recall} = \frac{TP}{TP + FN}
$$
  

$$
F1 = \frac{2 \times \text{Precision} \times \text{Recall}}{\text{Precision} + \text{Recall}}
$$

where  $TP$ ,  $FP$ , and  $FN$  are true positive, false positive, and false negative. We adopt the CodeWordNet of SYMLM [\[31\]](#page-14-4) to address label noise introduced by semantically similar words. More illustration of our evaluation metrics is provided in Appendix  $\S$ F for readers of interest.

Baselines. In this paper, we choose SYMLM, ASMDEPIC-TOR  $[33]$ , and XFL  $[52]$  as our baselines. For these three baselines, we train their models using our dataset under the same settings as SYMGEN to ensure a fair comparison. According to the XFL paper, it is only applicable to x86-64 binaries due to their design with VEX IR  $[52]$ . Therefore, we evaluate XFL and compare it with SYMGEN on x86-64 binary dataset. We have tried different training parameters for our baselines and selected the best performance following their training instructions. We skip evaluating NERO  $[14]$ , NFRE  $[20]$ , and DEBIN  $[23]$  as our baselines have demonstrated superior performance.

#### *B. RQ1: Overall Performance of* SYMGEN

To answer RQ1, we have evaluated SYMGEN on our dataset. [Table I](#page-9-1) presents its performance across the four computer architectures and four optimization levels. For each architecture and optimization level, we train an individual model, separately, and test it on our corresponding test set. Overall, SYMGEN achieves a precision of 0.275, a recall of 0.281, and an F1 score of 0.277 on average. For the x86-64 architecture, SYMGEN achieves an average precision of 0.373, a recall of 0.388, and an F1 score of 0.380. On the x86-32 binaries, SYMGEN records a precision of 0.324, a recall of 0.338, and an F1 score of 0.330. On MIPS and ARM, SYMGEN's performance yields precision scores of 0.136 and 0.265, recall scores of 0.138 and 0.260, and F1 scores of 0.137 and 0.262, respectively. In this performance, we have observed the performance discrepancy of SYMGEN between x86-64/x86-32 and ARM/MIPS binaries, which is the same as those of SYMLM. For instance, SYMGEN exhibits  $1.45 \times$  better performance on x86-64 binaries compared to ARM binaries, whereas SYMLM shows a  $1.37\times$ improvement under similar conditions. We hypothesize that this difference stems from the architecture-specific application binary interfaces (ABIs), which introduce more challenges in understanding MIPS/ARM binaries.

SYMGEN demonstrates optimal average performance on binaries optimized at the O3 level, achieving an F1 score of 0.290. Notably, SYMGEN performs better on binaries with higher optimization levels (O2 and O3) compared to those with lower levels (O0 and O1), with the exception of MIPS binaries. This improved performance can be attributed to the higher optimized binaries containing clearer function semantics and fewer invalid instructions, which aligns with observations made by Theodoridis *et al.* [\[64\]](#page-14-33).

<span id="page-8-0"></span>

(a) Preprocessed Comments

(b) LLM-generated Summaries

Figure 5: SYMGEN Performance with and without Domain Adaptation Using (a) Preprocessed Comments and (b) LLMgenerated Summaries

#### <span id="page-8-1"></span>*C. RQ2: Baseline Comparison*

To deepen our analysis, we also evaluate our baselines on our dataset, and the evaluation results are reported in [Table I.](#page-9-1) Generally, ASMDEPICTOR obtains the performance of 0.054 precision, 0.043 recall, and 0.047 F1 score on average. In contrast, SYMLM achieves the average precision of 0.114, recall of 0.099, and 0.105 F1 score across all computer architectures and optimization levels. XFL achieves an average precision of 0.189, recall of 0.156, and F1 score of 0.171.

More importantly, we observe the significant superiority of SYMGEN over all baselines, achieving up to 1650%, 1280%, and 1455.6% improvements in precision, recall, and F1 score, respectively. Specifically, SYMGEN is more effective than SYMLM, and its average precision, recall, and f1 score across all computer architectures and optimization levels are 241.2%, 283.8%, and 263.8% times these of SYMLM, respectively. Additionally, it outperforms ASMDEPICTOR with 409.3%, 553.5%, and 489.4% improvements on average precision, recall, and F1 score, respectively. For XFL, although it outperforms ASMDEPICTOR and SYMLM on x86-64 binaries, SYMGEN also achieves 197%, 249%, and 222% better precision, recall, and F1 score, respectively, compared to XFL. From the results, we observe that the performance metrics of baselines on our dataset are not as high as those reported in their respective papers. For instance, SYMLM achieved an average F1 score of 0.73 on its own dataset, which drops to 0.105 on our dataset. The F1 score of XFL also drops from 0.68 on the DEBIN dataset to 0.16 on our deduplicated dataset. As discussed in [§V-B,](#page-6-1) this discrepancy is largely due to data leakage issues in their test sets, which do not accurately reflect real-world performance when duplicates are effectively removed, as is the case with our dataset. This observation is consistent with findings from the unknown binary evaluation in the SYMLM paper [\[31\]](#page-14-4), where both SYMLM achieved F1 scores lower than 0.10 on unknown samples. ASMDEPICTOR also mentioned function deduplication in paper [\[33\]](#page-14-10). Similarly, XFL's performance drops significantly on unseen function names as presented in its paper [\[52\]](#page-14-5). That said, SYMGEN has dramatically advanced the state-of-the-art, showing much better effectiveness and generalizability.

<span id="page-9-1"></span>Table I: Overall Performance of SYMGEN and Our Baselines. Note that  $\uparrow$  and  $\downarrow$  indicate the improvement and deterioration of SYMGEN over baselines.

Arch	Model	Precision	Recall		F1 Score    Precision	Recall		F1 Score    Precision	Recall		F1 Score    Precision	Recall	F1 Score
			$\Omega$			01			<b>O<sub>2</sub></b>			<b>O3</b>	
x86-64	<b>SYMGEN</b> <b>SYMLM</b> <b>ASMDEPICTOR</b> XFL * SYMGEN V.S. ASMDEPICTOR SYMGEN v.s. SYMLM SYMGEN v.s. XFL	0.346 0.099 0.059 0.169 $1486.4\%$	0.356 0.100 0.043 0.140	0.351 0.100 0.050 0.153 $(1727.9%)$ (1602.0%) $(1249.5\%)$ $(1256.0\%)$ $(1251.0\%)$	0.376 0.101 0.067 0.188 $1461.2\%$	0.387 0.103 0.055 0.133 $(1603.6\%)$	0.381 0.102 0.060 0.156 $(1535.0\%)$ $(1272.3\%)$ $(1275.7\%)$ $(1273.5\%)$ $(1261.3\%)$ $\ $ (†104.7%) (†154.3%) (†129.4%) $\ $ (†100.0%) (†191.0%) (†144.2%) $\ $ (†95.4%)	0.383 0.106 0.061 0.196 $\uparrow$ 527.9%)	0.401 0.092 0.045 0.177 $(†791.1\%)$ $(1335.9\%)$ $(1300.0\%)$	0.392 0.098 0.052 0.186 $(1653.8\%)$ $(†126.6\%)$ $(†110.8\%)$	0.388 0.109 0.075 0.203 $1417.3\%$ $(1256.0\%)$ $(191.1\%)$	0.407 0.094 0.060 0.175 (†578.3%) $(1333.0\%)$ $(132.6\%)$	0.397 0.101 0.067 0.188 $1492.5\%$ $(1293.1\%)$ $(111.2\%)$
x86-32	<b>SYMGEN</b> <b>SYMLM</b> <b>ASMDEPICTOR</b> SYMGEN V.S. ASMDEPICTOR SYMGEN v.s. SYMLM	0.297 0.098 0.059 $1403.4\%$	0.315 0.114 0.045 $1600.0\%$	0.306 0.106 0.051 $(1500.0\%)$	0.314 0.138 0.061 $(1414.8\%)$	0.324 0.106 0.033 $(†881.8\%)$	0.319 0.120 0.043 (1641.9%) $(†203.1\%)$ (†176.3%) (†188.7%) $\  (+127.5\%)$ (†205.7%) (†165.8%) $\  (+208.1\%)$ (†167.7%)	0.342 0.111 0.057 $\uparrow$ 500.0%)	0.356 0.133 0.050 $1612.0\%$	0.348 0.121 0.053 (1556.6%) $(187.6\%)$	0.342 0.085 0.057 $1500.0\%$ $(1302.4\%)$	0.358 0.112 0.040 $(1795.0\%)$ (1219.6%)	0.349 0.096 0.047 $1642.6\%)$ (1263.5%)
<b>MIPS</b>	<b>SYMGEN</b> <b>SYMLM</b> <b>ASMDEPICTOR</b> <b>SYMGEN V.S. ASMDEPICTOR</b> SYMGEN v.s. SYMLM	0.144 0.124 0.056 $(157.1\%)$ $(†16.1\%)$	0.148 0.133 0.044 $\uparrow$ 236.4%) $(11.3\%)$	0.146 0.146 0.049 $*198.0\%)$ $(10.0\%)$	0.138 0.118 0.078 $(176.9\%)$ $(16.9\%)$	0.140 0.155 0.059 (137.3%) (16.7%)	0.139 0.139 0.067 $(10.0\%)$	0.130 0.150 0.050 $\uparrow$ 107.5%)    ( $\uparrow$ 160.0%) $\ $ (15.4%)	0.131 0.085 0.040 $227.5\%)$ †54.1%	0.130 0.108 0.045 $188.9\%$ $\uparrow$ 20.4%)	0.135 0.130 0.044 $1206.8\%$ $(13.8\%)$	0.134 0.071 0.036 $(1272.2\%)$ (188.7%)	0.134 0.091 0.040 $1235.0\%$ (147.3%)
ARM	<b>SYMGEN</b> <b>SYMLM</b> <b>ASMDEPICTOR</b> <b>SYMGEN V.S. ASMDEPICTOR</b> SYMGEN v.s. SYMLM	0.240 0.088 0.044 $(1445.5\%)$	0.238 0.042 0.044	0.239 0.057 0.044 $\uparrow$ 466.7%) ( $\uparrow$ 443.2%)	0.265 0.113 0.049 $1440.8\%$	0.257 0.061 0.037 $(1594.6\%)$	0.261 0.079 0.042 $\uparrow$ 521.4%)	0.268 0.135 0.030 (1793.3%)	0.269 0.097 0.033 (1715.2%)	0.269 0.113 0.032 $(1740.6\%)$	0.280 0.122 0.016 $\uparrow$ 1650.0%) $ (172.7\%)$ (1440.9%) (139.3%) $  (134.5\%)$ (1321.3%) (1230.4%) $  (198.5\%)$ (177.3%) (138.1%) $  (129.5\%)$	0.276 0.089 0.020 $1280.0\%$ $(1210.1\%)$	0.280 0.103 0.018 $(1455.6\%)$ $(171.8\%)$

\* For XFL, its design and implementations are limited to x86 64 binaries, as reported by its paper [\[52\]](#page-14-5). Therefore, XFL is only evaluated on our x86 64 dataset.

#### <span id="page-9-0"></span>*D. RQ3: Ablation Study*

Domain Adaptation by Function Summarization. The purpose of domain adaptation is to improve SYMGEN's capacities of binary code comprehension by mitigating the knowledge gap of our target pretrained LLMs. To validate its effectiveness, we perform the comparative evaluations on SYMGEN by studying its performance with and without domain adaptation (1) the function comments that have been preprocessed with noise removal and (2) the function summaries generated by LLMs. For SYMGEN without domain adaptation, we directly perform finetuning on function name inference with parameter-efficient learning.

We first present the results of domain adaptation using our preprocessed function comments in [Figure 5a.](#page-8-0) This domain adaptation strategy enhances SYMGEN's recall and F1 score by 4.86% and 1.82% respectively, but it results in a decline in precision by 1.21%. In contrast, as shown in [Figure 5b,](#page-8-0) domain adaptation with LLM-generated summaries improves SYMGEN across all metrics, with increases of 4.53%, 8.21%, and 6.36% in precision, recall, and F1 score, respectively. Through cross-comparison, we observe that LLM-generated summaries are more effective than function comments, enhancing precision by 5.81%, recall by 3.19%, and the F1 score by 4.46%. Overall, these findings indicate that domain adaptation can significantly enhance SYMGEN's performance in function name prediction. Furthermore, we also note that SYMGEN without domain adaptation tends to generate meaningless function names, such as FUN\_876458. After performing domain adaptation, such meaningless function names are significantly reduced by 71.7% in inference results.

Parameter-efficient Learning. Parameter-efficient learning significantly reduces both the training duration and GPU memory demands for SYMGEN. For context, the full-parameter pretraining of the Llama model necessitates the use of 2,048 A100 GPUs, each equipped with 80 GB of VRAM, spanning approximately 21 days—an aggregate of 163,840 GB of

<span id="page-9-2"></span>

Figure 6: Performance of SYMGEN and ASMDEPICTOR Using Decompiled Code and Assembly Code as Input

GPU VRAM [\[66\]](#page-15-4). Moreover, finetuning the Llama model is recommended to have at least 8 A100 GPUs, cumulatively amounting to 640 GB of VRAM [\[59\]](#page-14-34). In contrast, leveraging our parameter-efficient learning approach enables the training of SYMGEN with the Code Llama 34B model on only one A100 GPU within a considerably reduced timeframe of around 37 hours. This stark reduction in resource utilization underscores the efficiency of our method, making it a viable solution for environments with limited hardware capabilities. In addition, we also observe that parameter-efficient learning can effectively assist SYMGEN to converge. While setting the total number of epochs as 16, we observe the model training converges at about 6 epochs. [Figure 11](#page-17-1) (in Appendix  $\S G$ ) presents the training loss of SYMGEN across the training epochs. This early stop can effectively avoid overfitting of SYMGEN on our training dataset, particularly in the context of the supervised learning process.

Decompiled Code as Binary Representations. Given the superior performance of SYMGEN over our baselines, we examine our design choice of using decompiled code as model

Model	<b>Training Set</b>	Precision	Recall	<b>F1 Score</b>
<b>ASMDEPICTOR</b>	w/ duplicates	0.019	0.015	0.017
	w/o duplicates	0.059	0.043	0.050
<b>SYMLM</b>	w/ duplicates	0.117	0.068	0.086
	w/o duplicates	0.099	0.100	0.100
XFL	w/ duplicates	0.159	0.144	0.151
	w/o duplicates	0.169	0.140	0.153
<b>SYMGEN</b>	w/ duplicates	0.353	0.329	0.341
	w/o duplicates	0.346	0.356	0.351

<span id="page-10-0"></span>Table II: Performance of SYMGEN and Baselines on Training Set with and without Duplicates

<span id="page-10-1"></span>Table III: Performance of SYMGEN and Baselines on Test Set with and without Duplicates

Model	<b>Test Set</b>	<b>Precision</b>	Recall	<b>F1 Score</b>
<b>ASMDEPICTOR</b>	w/ duplicates	0.057	0.042	0.048
	w/o duplicates	0.059	0.043	0.050
<b>SYMLM</b>	w/ duplicates	0.103	0.093	0.097
	w/o duplicates	0.099	0.100	0.100
XFL	w/ duplicates	0.152	0.138	0.144
	w/o duplicates	0.169	0.140	0.153
<b>SYMGEN</b>	w/ duplicates	0.348	0.369	0.358
	w/o duplicates	0.346	0.356	0.351

input, by comparing it to the use of assembly code as input. For cross-comparison, we have also enhanced our baseline models by retraining them on our decompiled code dataset and using the same experimental setup, described in [§VI-A.](#page-7-2) Here, we choose ASMDEPICTOR as the baseline for this ablation study due to several reasons. First, we could not adapt SYMLM to decompiled code since it was originally trained on a microtracebased model with specific input representations designed for assembly code [\[31\]](#page-14-4), which cannot be applied to decompiled code. XFL's binary analysis process is based on VEX IR, which cannot be adapted to decompiled code as well. Consequently, we focus on ASMDEPICTOR and retrain it using the exact same configurations outlined in its original paper. We only modify its input to decompiled code, with all other settings unchanged. For comparison, we updated SYMGEN's domain adaptation and fine-tuning processes to account for nuances specific to handling assembly code as input. This involves modifying the prompts used during finetuning to align with assembly code, *e.g.*, the prompt for finetuning is shown in Figure  $9b$  (in Appendix  $\S$ C).

[Figure 6](#page-9-2) presents the evaluation results. For ASMDEPICTOR, the decompiled code improves its precision, recall, and F1 score by 12.1%, 36.0%, and 24.6%. In contrast, with decompiled code serving as the binary function representation, SYMGEN achieves 333.7% improvement in precision, 304.2% in recall, and 317.6% in F1 score over the assembly code input variant. Moreover, for the same decompiled input, SYMGEN is also much more effective than ASMDEPICTOR, *i.e.*, with the 404.1%, 470.6%, and 435.2% better precision, recall, and F1 score, respectively. Considering our objective on generalizability, we attribute the outperformance of SYMGEN to its unique problem formalization as well as the efficient and generalizable training design.

In-dataset Deduplication. To investigate whether in-dataset duplicates, which were defined in  $\frac{\partial V}{\partial \theta}$ , in the training set

<span id="page-10-2"></span>Table IV: Performance of SYMGEN, SYMLM, and XFL on Obfuscated Binaries

<b>Obfuscation</b>	<b>SYMGEN</b>	<b>SYMLM</b>	XFL
w/o obfuscation	0.302	0.071	0.139
bcfobf	$0.288(-4.6\%)$	$0.063(-11.3%)$	$0.044 (-215.9\%)$
cffobf	$0.271(-10.3\%)$	$0.056(-21.1\%)$	$0.034$ ( $-308.8\%$ )
subobf	$0.303 (+0.3\%)$	$0.062(-12.7%)$	$0.039(-256.4%)$

can affect model performance, we also train our baselines and SYMGEN using training sets with and without such duplicates. [Table II](#page-10-0) presents the evaluation results. Overall, we observe that these duplicates significantly reduce the performance of our baselines, while they have negligible impact on SYMGEN. For instance, the F1 scores of SYMLM, XFL, and ASMDEPICTOR without training set duplicates are  $1.23 \times$ ,  $1.01 \times$ , and  $2.94 \times$ better compared with duplicates. Meanwhile, SYMGEN achieves an F1 score of 0.341 with duplicates in the training set, which is 2.6% lower than the performance without duplicates. While we could have compared SYMGEN with baselines on the withduplicate setting to show better outperformance, we chose to deduplicate training sets to give a fair comparison and report baselines' better performance. In-dataset duplicates can cause the model to overfit on the training set and memorize these repeated data rather than learning generalizable features. This results in poor performance when the model encounters unseen data. Such overfitting is also widely noted by prior research [\[3\]](#page-13-20), [\[34\]](#page-14-35), [\[9\]](#page-13-10), [\[37\]](#page-14-18).

In addition to training sets, we have also evaluated the in-dataset duplicate removal for test sets. [Table III](#page-10-1) presents the performance of SYMGEN and baselines on the duplicated test set, where the F1 scores of ASMDEPICTOR, SYMLM and XFL are 0.048, 0.097, and 0.144, respectively. In contrast, their F1 scores without test set duplicates are 0.050, 0.100, and 0.153, showing minor differences of only 3.09%, 4.0%, and 5.8%. Similarly, SYMGEN achieves an F1 score of 0.358 on the duplicated test set, compared to 0.351 on the non-duplicated test set. This indicates that in the duplicated test set, despite some function names appearing more frequently than others, the final performance of the models is not significantly impacted.

# <span id="page-10-3"></span>*E. RQ4: Effectiveness on Real-world Binaries*

Obfuscated Binaries. Binary Obfuscation is widely used to increase the complexity of the code, making binary analysis and reverse engineering more difficult. To evaluate the obfuscation resistance performance of SYMGEN and baselines, we reuse SYMLM's obfuscated binary dataset with the same evaluation setup as [§VI-A.](#page-7-2) Specifically, this obfuscation dataset consists of 363 obfuscated binaries from 6 GNU projects (*i.e.*, findutils, coreutils, curl, less, putty, and bash). Following prior works  $[31]$ ,  $[53]$ ,  $[54]$ , we have used Hikari [\[46\]](#page-14-36) to obfuscate the binaries three obfuscation options, *i.e.*, bogus control flow (bcfobf), control flow flattening (cffobj), and instruction substitution (subobf). For each obfuscation option, we leverage its default obfuscation configurations. To analyze SYMGEN's performance affected by obfuscation, we also include the original binaries (without obfuscation) in our evaluation dataset.

[Table IV](#page-10-2) presents the performance on obfuscated binaries of SYMGEN, SYMLM and XFL. For simplicity, we also skip

presenting ASMDEPICTOR's performance as it has shown the worse performance in [§VI-C.](#page-8-1) For SYMGEN, it achieves 0.288, 0.271, and 0.303 average F1 scores on the obfuscated binaries, which shows the 488% better F1 score than SYMLM and 777% better than XFL. Compared with the performance of SYMGEN on the original binaries (w/o obfuscation), SYMGEN's performance decreases by 4.6% and 10.3% on bcfobf and cffobf binaries. This degradation is much lower than these in SYMLM and XFL. For instance, the performance of XFL decreases by 308.8% in cffobf. This demonstrates SYMGEN's better obfuscation resistance than SYMLM and XFL. Moreover, when SYMGEN achieves the same F1 score on the subobf binaries compared as the original ones, SYMLM and XFL degrades on these binaries by 12.7% and 256.4%, respectively. We attribute the better performance of SYMGEN to its specific generalizable learning paradigm with the domain-adapted generative model.

Malware Executables. To further understand SYMGEN's generalizability, we have used it to predict function names for malware executables. For this, we first collect all Linux malware projects (*e.g.*, backdoors, botnets, infectors, and Mirai) from the prevalent open-source malware repository [\[70\]](#page-15-16). We then follow its compilation instructions, resulting in 16 x86-64 binaries, which are further stripped as our test binaries. To apply SYMGEN, we directly use the model that we have trained *on our own dataset* to generate the function names for all 812 malware binary functions. Surprisingly, SYMGEN achieves 0.320 precision, 0.381 recall, and 0.348 F1 score. Considering the training dataset of SYMGEN includes projects of the GNU repository, which have completely different project context from the test malware binaries, this performance is remarkably close to the one that we have reported in [§VI.](#page-7-3) We believe it demonstrates a strong generalizability when SYMGEN encountered the unknown malware binaries. To understand why SYMGEN can successfully predict malware function names, we further study and discuss several malware functions in [§VII-B.](#page-11-0)

# VII. FURTHER ANALYSIS

In addition to reporting the aggregate statistical performance of SYMGEN, we study individual cases of the prediction results to understand its prediction errors and generalizability and report our findings in this section.

# <span id="page-11-2"></span>*A. Concrete Cases and Inference Errors*

[Table V](#page-12-0) presents the predicted function names of SYMGEN and our baselines, sampled from our inference results. In this table, both ground truth and SYMGEN predicted function names are tokenized, consistent with our baselines' outputs, though SYMGEN produces function names in the same complete form as ground truth. From [Table V,](#page-12-0) we observe that SYMGEN can accurately infer the function name for some binary functions (*e.g.*, split\_string), while baselines cannot learn the correct function semantics and generates wrong names. Additionally, SYMGEN is capable of inferring the core semantics of other functions, generating names that capture essential semantics but may omit or add some details. For example, SYMGEN correctly identifies the key semantics of check and dir for the binary function gc\_ide\_check\_dir but misses ide and the project-specific word gc, while our baselines unfortunately fail to grasp the function semantics.

Inference Errors. Despite we have advanced the-state-of-art in inferring binary function names, we still observe inference er-

```
1 void set_uint32(char *cp,uint32_t v)
      \left\{ \right.3 *(\text{uint32}_t, \text{c}) cp = v;<br>4 return:
           4 return;
5 }
         (a) Decompiled Code before Binary Stripping
1 void FUN_05e4a017(undefined4 *param_1,undefined4 → param 2)
          param_2)
2 {
           *param_1 = param_2;4 return;
5 }
          (b) Decompiled Code after Binary Stripping
```
# Figure 7: An Example of Semantic Loss in Stripped Binaries. The key semantics of **set\_uint32** is preserved in its type (line 3), which is removed in the stripped binary.

rors in our results. Beyond the aforementioned issues of omitted or additional words, we identify the other two common error sources, including (*i*) semantic loss due to binary stripping and (*ii*) out-of-vocabulary (OOV) words. In the process of binary stripping, the symbols, such as variable types, are removed. For instance, [Figure 7](#page-11-1) presents the decompiled code of the function set\_uint32 from the GNU coreutils-8.32 project. In [Figure 7a,](#page-11-1) the variable cp is casted into the type uint\_32 at line 3. This function is named as set\_uint32 to reflect such type casting operation. However, in [Figure 7b,](#page-11-1) the variable type is stripped, leading to a huge semantic gap before and after stripping. SYMGEN predicts the function name as assign, which is treated as an inference error in our evaluations. Another common inference error comes from the out-of-vocabulary words that are specific to projects. For example, in our dataset, the test functions from the adns and dico projects mostly feature the prefixes adns\_ and dico\_ in their ground truth names. These words are unknown to our model and cause inference errors. One example function is dico\_handle\_command, for which SYMGEN predicts its name as do\_command. Although SYMGEN correctly infers the key semantics of this function, our calculated precision, recall, and F1 score for this function are merely 50.0%, 33.3%, and 40.0%, respectively. While we could identify and remove such prefixes to improve SYMGEN's performance, we chose not to, aiming to more accurately reflect real-world use cases.

# <span id="page-11-0"></span>*B. Generalizability and Effectiveness*

Generalizability on Malware Function Name Prediction. We first zoom into individual malware functions, because SYMGEN achieves impressive generalizability, especially on malware executables (presented in [§VI-E\)](#page-10-3). Our analysis identifies that the function names of some functions, highly related to malware, are accurately predicted, *e.g.*, command\_flood and get\_random\_ip. For instance, [Figure 8](#page-12-1) presents the decompiled code for the getRandomPublicIP function, and [Figure 12](#page-17-3) (in Appendix [§H\)](#page-17-4) presents the source code of the function. This function generates a random public IPv4 address. If a valid range is available in ipState, it increments the fourth part to create and return a new address. Otherwise, it randomly generates an address, ensuring it avoids reserved

<span id="page-12-0"></span>

<b>Ground Truth</b>	<b>SYMGEN</b>	<b>ASMDEPICTOR</b>	<b>SYMLM</b>	XFL
gc ide check dir	check dir	remove temp	mu init check parse	mu check file
split string	split string	copy string to string	print write file add	mu str set
client session	ssl session load	skip	file write read output	s type check pid
ftp time left accept	get time left	string to	read parse file get	get time read
write error stderr	print error	v print f	print gmp c add	print standard err
read file to buffer	do read file	expand	ipmi monitor ctx	test get padding file
elf type	parse type	expand assignment	p be type	get set ip
gcd odd	gcd odd	mpn copy	u s n gmp	mpn is get
host hash	hash string	png index of number	be p s get	mpz listen

Table V: Examples of Inferred Function Names.

ranges until a valid address is found. Finally, it returns the generated IP address with the last octet set to 0.

After binary stripping, the variable names in the original code have all become meaningless names, like DAT 0020e389 and iVar1, and the function name is replaced with FUN\_041324a. However, the execution flow of the function has not changed. Surprisingly, SYMGEN precisely infers function semantics from this decompiled code and generates the function name get\_random\_ip. Although the token public is missed, we still can get the crucial semantics of random IP generation via its name, which is particularly helpful for malware analysts. Note that, botnets can be used to perform DDoS attacks, steal data, send spam, and allow the attacker to access victim devices [\[75\]](#page-15-17).

Retrieval-augmented Function Name Generation. Our analysis of SYMGEN's outputs also reveals its capability to directly extract key semantic information from the function body, when available. For instance, when we insert the  $uint32$  t type at line 3 in [Figure 7a,](#page-11-1) SYMGEN is able to accurately predict the modified function's name as writ\_uint32\_t. To validate if this finding is generalizable, we use the IoT firmware binaries as the test data. For this, we collect 935 test functions from 9 firmware binaries (with ground truth) that are used by existing firmware analysis research [\[19\]](#page-13-21). Notably, the firmware function names have many OOV words, *e.g.* IoT-specific terms. Given the IoT binaries, we test SYMGEN using decompiled functions with and without symbols (*i.e.*, variable names and types). We find that the symbols can boost SYMGEN's performance by 258.6%.

In natural language processing, such behavior is defined as retrieval-augmented generation (RAG) [\[36\]](#page-14-37). RAG is a process where LLMs can integrate information (*e.g.*, words and phrases) into its output, retrieved from input to enhance the quality of its output. We attribute the emergent RAG capacity of SYMGEN to its two key design advantages. The first advantage lies in SYMGEN's novel generative learning design. Moreover, SYMGEN utilizes decompiled code as binary code representations. The decompiled code can be enhanced by decompilers. Although the stripped binaries can barely have symbols, SYMGEN can be boosted by the outcomes of existing binary symbol recovery frameworks, *e.g.*, variable type and name inference models [\[10\]](#page-13-7), because of its RAG capacity. This property will benefit other large-scale security analyses [\[30\]](#page-14-38).

# VIII. DISCUSSION

This section outlines the limitations of our study and proposes directions for future research.

Base Model. We employ the pretrained Code Llama as SYMGEN's base model. Given the rapid advancements in

```
1 void get_random_ip (void) {
 2 ...
    \textbf{if} ((DAT_0020e389 == 0) (DAT_0020e38c == -1)) {
      iVar1 = rand();
      DAT_0020e389 = (char)iVar1 + (char)(iVar1 / 0xff);\begin{array}{ccc} 6 & & \dots \end{array}DAT_0020e38c = ' \0';8 while ((((((((DAT_0020e389 == 0))
                     (DAT_0020e389 == 10)10 \qquad \qquad \bullet \qquad \bullet \bullet11 (0xdf < DAT_0020e389)))))
12 {
13 iVar1 = rand();
14 DAT_0020e389 = (char)iVar1 + (char)(iVar1 / 0xff);
15 ...
16 }
```
Figure 8: The Decompiled Code with SYMGEN Predicted Function Name (Highlighted) for a Botnet Binary Function. Its ground truth name is **getRandomPublicIP**.

the field of generative large language models (LLMs), it is conceivable that future developments could introduce models with superior capabilities in understanding binary code semantics. Furthermore, enhancing SYMGEN with additional model parameters might further augment its performance. Considering SYMGEN's adaptable design to any generative LLMs, exploring alternative base models can be a promising research topic.

Decompiler. In this study, we employ Ghidra, the most popular open-sourced decompiler, to generate decompiled code for SYMGEN. It is important to acknowledge that such decompiler outputs may include errors inherent to the decompilation process. Additionally, the output from different decompilers can vary, potentially introducing biases. Despite SYMGEN's design being decompiler-agnostic, exploring SYMGEN with outputs from alternative decompiler output presents a valuable avenue for future research.

Performance and Inference Errors. The errors introduced by binary stripping and OOV words ([§VII-A\)](#page-11-2) remain challenging to resolve due to the missing semantics. Unlike simpler classification tasks, *e.g.*, binary classification, SYMGEN generates each word of function names from the entire English vocabulary, encompassing  $>170K$  unique words [\[43\]](#page-14-39). This complexity significantly increases the difficulty of accurately predicting function names for SYMGEN. Additionally, SYMGEN's evaluation is conducted at the word level; thus, any inaccuracies in word prediction are rigorously counted in our results, even if those inaccuracies have minimal semantic impact. Despite these rigorous standards, SYMGEN has achieved an

overall F1 score of 0.277, marking a substantial advancement over prior works and a remarkable step towards practicality.

Dataset Quality. Dataset quality is crucial in model training. Besides our efforts in data deduplication and leakage mitigation, low-quality or meaningless function names can also downgrade dataset quality. For instance, some functions are automatically generated by the compiler and linker. Their names do not accurately reflect the function semantics. To address this, we made attempts to manually remove these meaningless functions from our dataset. Moreover, we also found some words appear more frequently than others without carrying much semantics. For example, 4.5% of all function names contain "set". These function names usually consist of three to five tokens, and "set" represents only 1.2% of the total tokens. We believe the rigorous approach to address this quality issue requires manual annotation. However, it is very time-consuming and relies heavily on the programmer's experience and judgment. Automating the identification of such functions to further enhance dataset quality presents a potential research direction.

Computational Cost. We have also measured the computational cost for training and offline inference. Specifically, the training and reproduction of our baselines, including ASMDEPICTOR, SYMLM, and XFL, take 14, 30, and 32 hours per model, respectively, while SYMGEN costs 41 hours. For offline inference, the time per sample is 0.2, 1, 1, and 3 seconds for ASMDEPICTOR, SYMLM, XFL, SYMGEN. In addition to the time cost, the hardware resources are the same for SYMGEN and our baselines as we used the same evaluation settings. Note that the training cost is a one-time effort, *i.e.*, the model is trained once but can be used for inference many times. While SYMGEN is computationally more expensive due to its larger model size, we believe model compression techniques [\[15\]](#page-13-22), *e.g.*, model quantization and distillation [\[55\]](#page-14-40), can be applied to reduce this cost.

# IX. CONCLUSION

We have presented SYMGEN, a novel framework designed for inferring function names in stripped binaries with the new problem formalization. Unlike the classification-based approaches, SYMGEN is the first to employ a generative learning paradigm to enhance model generalizability for realworld binaries. Our extensive evaluations reveal that SYMGEN surpasses current state-of-the-art models in effectiveness with better obfuscation resistance. Our ablation studies further validate the effectiveness of SYMGEN's key components.

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#### APPENDIX

#### <span id="page-15-7"></span>*A. Noises in Function Comments*

As stated in  $\S$ IV-A, we have observed the following five categories of noises during our manual analysis of the sampled source code and comments.

- Incomplete and Manipulated Comment. Some comments are in the form of incomplete comments which do not express the full semantics of function semantics. Moreover, some developers incorporate the comment content generated with HTML tags for documentation auto-generation purposes or include URLs for external references, which have changed the original comments created by developers.
- Wordy Comment. Some comments come with noisy details that are not expressed in the function bodies, such as the link

to some documentation and specific explanations of function arguments, variables, and configurations.

- Non-English Comment. In this paper, we focus on the function comments in English. We observe that some developers write comments in their native languages, sometimes mixing these with English within the comments.
- Comment for Communications. Some comments are primarily used for communication among developers, rather than just summarizing functionalities. For example, developers use keywords like "TODO" and "FIXME" as the message to inform their co-workers about the next steps.
- Irrelevant Comment. We have also observed that some comments are not relevant to the function semantics, which are purely noises. Some of these comments can be the ones that are under construction, meaning that the developers have not finished the comment, therefore, the comments themselves appear to be meaningless.

#### <span id="page-15-8"></span>*B. Constructing Abstract Syntax Trees for Source Functions*

To construct Abstract Syntax Trees (ASTs) for source code, a parser is needed to interpret the source structure. We build a C code parser based on ANTLR 4 [\[49\]](#page-14-30) by adapting a C language grammar file<sup>[4](#page-15-18)</sup>. Provided with source code, the parser will produces an AST representing the code's structure. Each explicitly defined grammar rule in the grammar file corresponds to a node in the AST. For instance, the following rule defines the grammar of function declaration.

```
1 functionDefinition:
2 declarationSpecifiers? declarator
     ,→ declarationList? compoundStatement;
```
In this rule, *declarationSpecifiers*, *declarator*, *declarationList*, and *compoundStatement* represent the function's return type, name, input arguments, and body, respectively. Each of these components is further defined by its own set of rules. If a function declaration appears in the source code, a node of type *functionDefinition* will be created in the AST by parser, with four child nodes representing these attributes.

After generating the AST, we can perform a depth-first search (DFS) to traverse each node, aiming to collect all function bodies in the source code. The algorithm is presented in [algorithm 2.](#page-16-5) The traversal begins at the AST root using function CollectFunctions (lines 8 to 12). As each node is visited, the code checks if the node's type is *functionDefinition*. If true, this indicates that the node corresponds to a function declaration, allowing us to collect its content, which represents the function body (lines 2–7).

# <span id="page-15-10"></span>*C. Prompts for Function Summary Generation and Function Name Inference*

The choice of prompt is especially critical to the results of the model. For generating function summary and predicting function name, we tried many different prompts on a small dataset and finally chose the prompts with best performance. For function summary task, [Figure 9a](#page-16-0) presents the prompt we used. For predicting function name, the prompts with assembly code and decompiled code as input are shown in [Figure 9b](#page-16-0) and [Figure 9c.](#page-16-0) As mentioned in [§IV-C,](#page-5-2) the original artificial function name in decompiled code is replaced by the [MASK]

<span id="page-15-18"></span><sup>4</sup><https://github.com/antlr/grammars-v4/blob/master/c/C.g4>

#### Algorithm 2: Collect Functions from AST

<span id="page-16-5"></span>

	1 <b>initialize</b> funcMap := {}					
	2 <b>Function</b> CollectFunctions (node):					
3	<b>if</b> node.type == functionDefinition <b>then</b>					
$\overline{\mathbf{4}}$	$funcMap[node.name] := (node. args, node. content)$					
5	for each <i>child</i> in <i>node</i> . Children <b>do</b>					
6	CollectFunctions ( <i>child</i> )					
	return					
	<b>8 Function</b> $DFS()$ :					
9	$root := \texttt{qetASTRoot}()$					
10	CollectFunctions (root)					
11	return					

<span id="page-16-1"></span>Table VI: Source Projects and Binaries in Our Dataset



token without altering any other portion of the decompiled code (*e.g.*, function body and function parameters.)

# <span id="page-16-2"></span>*D. Source Project and Binaries in* SYMGEN *dataset*

[Table VI](#page-16-1) presents the 33 open-source projects that we used to build our dataset, which contains 9,842 unique binaries in total. We have chosen well-known GNU projects, including bash, wget, and so on. bash is a Unix shell and command language developed as part of the GNU Project. It provides a command-line interface for users to interact with their operating system, offering powerful scripting capabilities and extensive support for automating tasks. wget is a command-line utility for downloading files from the web. It supports downloading via HTTP, HTTPS, and FTP protocols, and can recursively download entire directories with ease. These projects are widely used and highly representative.

#### <span id="page-16-3"></span>*E. Substitution in Function Body*

In the decompiled code, the callee function names will affect the identification of the same function body. Due to the lack of debuging information, the function names in the function call sentences is the combination of FUN and its address. [Figure 10a](#page-17-0) presents the function add\_intrinsic before preprocessing.

Using ANTLR 4 to build the AST (Appendix  $\S$ B), then we substitute these callee function names with consistent names.

<span id="page-16-0"></span>



(c) Prompt of Decompiled Code as Input

# Figure 9: Prompts for Summary Generation and Function Name Prediction.

[Figure 10b](#page-17-0) presents the function after preprocessing. New function names are based on the order of appearance.

#### <span id="page-16-4"></span>*F. Illustration of Our Evaluation Metrics and CodeWordNet*

To help readers understand how our evaluation metrics (defined in [§VI-A\)](#page-7-2) work, we use the function ftp\_timeleft\_accept in forth line of [Table V](#page-12-0) as an example to demonstrate how we calculate the scores. First, we will divide the function name into a series of tokens. For example, ftp\_timeleft\_accept is divided into ftp, time, left, accept and our predicted name is divided into get, time, left. Then we calculate TP, FP and FN. True positive (TP) refers to a result in binary classification where the model correctly identifies a positive instance as positive. False positive (FP) refers to the incorrect identification

```
1 void [MASK](undefined8 param_1,undefined8
    ,→ param_2)
2 {
3 undefined8 uVar1;
4
5 uVar1 = FUN_00000c4b(param_1);6 uVar1 = FUN_00003016(uVar1);7 FUN_00003086(uVar1,param_2);
      FUN_00003086(uVar1,0);9 return;
10
```
# (a) The Decompiled Code of **add\_intrinsic**

```
1 void [MASK](undefined8 param_1,undefined8
     ,→ param_2)
2
3 {
4 undefined8 uVar1;
5
6 uVar1 = FUN_0(param_1);7 uVar1 = FUN_1(uVar1);FUN_2(uVar1,param_2);
      FUN_2(uVar1,0);10 return;
11 }
```
(b) The Decompiled Code of **add\_intrinsic** after Processing

<span id="page-17-1"></span>Figure 10: Comparison of Decompiled Code Before and After Processing



Figure 11: Training Loss across Epochs

of something as belonging to a certain category, while false negative (FN) is the failure to identify something as belonging to that category when it actually does. In this example, token time and left belong to true positive and get belongs to false positive. The predicted name missed  $ftp$  and accept, both of which belong to false negatives. So the precision, recall and F1 score of this function are 0.67, 0.50 and 0.57. We have used the CodeWordNet module of SYMLM [\[31\]](#page-14-4) to precisely match semantically similar words. Specifically, CodeWordNet is trained on the word embedding models which can generate the close embeddings for morphological words, *e.g.*, synonyms. Using CodeWordNet, SYMGEN can precisely calculate the scores based on its embedding models.

# <span id="page-17-2"></span>*G. Training Loss across Epoches*

When applying parameter-efficient training, we observe the early-stop effect during training. [Figure 11](#page-17-1) presents the training loss of SYMGEN across the training epochs. The loss reaches a stable status at 6 epochs, while our default setting is 16.

```
1 in addr t getRandomPublicIP()
2 {
3 if(ipState[1] > 0 && ipState[4] < 255)
 4 \qquad \qquad \{5 ipState[4]++;
 6 char ip[16] = {0};
7 szprintf(ip, "%d.%d.%d.%d", ipState[1],
                ,→ ipState[2], ipState[3], ipState[4]);
8 return inet_addr(ip);
 9 }
11 ipState[1] = rand() % 255;<br>12 ibState[2] = rand() % 255;ipState[2] = rand() % 255;13 ipState[3] = rand() % 255;<br>14 ipState[4] = 0;ipState[4] = 0;15 while(
16 (ipState[1] == 0)
17 (ipState[1] == 10)
18 (ipState[1] == 100 && (ipState[2] >= 64
                \leftrightarrow && ipState[2] <= 127))
19 (ipState[1] == 127)
20 (ipState[1] == 169 \&\&\&\&\quad ipState[2] == 254)
21 (ipState[1] == 172 && (ipState[2] <= 16\leftrightarrow && ipState[2] <= 31))
22 (ipState[1] == 192 && ipState[2] == 0 &&
                \leftrightarrow ipState[3] == 2)
23 (ipState[1] == 192 && ipState[2] == 88
                \leftrightarrow && ipState[3] == 99)
24 (ipState[1] == 192 && ipState[2] == 168)
25 (ipState[1] == 198 && (ipState[2] == 18
                \rightarrow ipState[2] == 19))
26 (ipState[1] == 198 && ipState[2] == 51
                \leftrightarrow && ipState[3] == 100)
27 (ipState[1] == 203 && ipState[2] == 0 &&
                \rightarrow ipState[3] == 113)
28 (ipState[1] >= 224)
29 )
30 {
31 ipState[1] = rand() % 255:
32 ipState[2] = rand() % 255;
33 ipState[3] = rand() % 255;
34 }
36 char ip[16] = {0};
37 szprintf(ip, "%d.%d.%d.0", ipState[1],
         ,→ ipState[2], ipState[3]);
38 return inet_addr(ip);
39 }
```
Figure 12: The Source Code of **getRandomPublicIP**

# <span id="page-17-4"></span>*H. Source Code of Malware Function*

[Figure 12](#page-17-3) presents the source code of function getRandomPublicIP in botnet.

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